**IN THE CLAIMS:**

Please amend the claims as follows:

Sub D1

1. (Twice Amended) A display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2, · · ·, Sn, Sn+1, Sn+2, · · ·, S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, · · ·, GmL; and

C1

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, · · ·, GmR, wherein:

the pixels connected to the source signal lines S1, S2, · · ·, Sn are supplied with the selection signals from the first gate signal lines G1L, G2L, · · ·, GmL;

the pixels connected to the source signal lines Sn+1, SN+2, · · ·, S2n are supplied with the selection signals from the second gate signal lines G1R, G2R, · · ·, GmR;

the selection signal starts to be supplied to the second gate signal line G1R while the selection signal is supplied to the first gate signal line G1L; and

the selection signal starts to be supplied to the first gate signal line G2L while the selection signal is supplied to the second gate signal line G1R,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

2. (Amended) A display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2, .... , Sn, Sn+1, Sn+2, .... , S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, .... , GmL; and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, .... , GmR, wherein:

the pixels connected to the source signal lines S1, S2, ....., Sn are supplied with the selection signals from the first gate signal lines G1L, G2L, .... , GmL;

the pixels connected to the source signal lines Sn+1, Sn+2, ..... , S2n are supplied with the selection signals from the second gate signal lines G1R, G2R, ..... , GmR; and

the selection signals are sequentially supplied to the first gate signal line G1L, the second gate signal line G1R, the first gate signal line G2L, the second gate signal line G2R, ..... , the first gate signal line GmL, and the second gate signal line GmR in this order with a delay of a half period between the respective adjacent gate signal lines,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

23. (Amended) A method of driving an active matrix display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2, .... , Sn, Sn+1, Sn+2, .... , S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, .... , GmL; and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, .... , GmR, wherein said method comprises the steps of:

C3 cont.

supplying the pixels connected to the source signal lines S1, S2, ...., Sn with the selection signals from the first gate signal lines G1L, G2L, ....., GmL;

supplying the pixels connected to the source signal lines Sn+1, Sn+2, ...., S2n with the selection signals from the second gate signal lines G1R, G2R, ...., GmR;

starting to supply the selection signal to the second gate signal line G1R while the selection signal is supplied to the first gate signal line G1L; and

starting to supply the selection signal to the first gate signal line G1L while the section signal is supplied to the second gate signal line G1R,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

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24. (Twice Amended) A method of driving an active matrix display device comprising:

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a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2, · · ·, Sn, Sn+1, Sn+2, · · ·, S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, · · ·,

GmL; and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, · · · , GmR, wherein said method comprises the steps of:

supplying the pixels connected to the source signal lines S1, S2, · · · , Sn with the selection signals from the first gate lines G1L, G2L, · · · , GmL;

supplying the pixels connected to the source signal lines Sn+1, Sn+2, · · · , S2n with the selection signals from the second gate lines G1R, G2R, · · · , GmR; and

C4 cont.

sequentially supplying the selection signals to the first gate signal line G1L, the second gate signal line G1R, the first gate signal line G2L, the second gate signal line G2R, · · · , the first gate signal line GmL, and the second gate signal line GmR in this order with a delay of a half period between the respective adjacent gate signal lines,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

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25. (Amended) A display device comprising:

C5

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2, · · · , Sn, Sn+1, Sn+2, · · · , S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, · · · , GmL; and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, · · · , GmR, wherein:

the pixels connected to the source signal lines S1, S2, · · ·, Sn are supplied with the selection signals from the first gate signal lines G1L, G2L, · · ·, GmL;

the pixels connected to the source signal lines Sn+1, SN+2, · · ·, S2n are supplied with the selection signals from the second gate signal lines G1R, G2R, · · ·, GmR;

the selection signal starts to be supplied to the second gate signal line G1R while the selection signal is supplied to the first gate signal line G1L; and

the selection signal starts to be supplied to the first gate signal line G2L while the selection signal is supplied to the second gate signal line G1R,

wherein the m first gate signal lines G1L, G2L, · · ·, GmL of the first gate driver are not connected to the m second gate signal lines G1R, G2R, · · ·, GmR of the second gate driver, and

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

C5 cont

26. (Amended) A display device comprising:

a pixel portion in which (m x 2n) pixels are arranged in matrix form (both m and n are natural numbers);

a source driver for supplying video signals to 2n source signal lines S1, S2, · · ·, Sn, Sn+1, Sn+2, · · ·, S2n;

a first gate driver for supplying selection signals to m first gate signal lines G1L, G2L, · · ·, GmL; and

a second gate driver for supplying selection signals to m second gate signal lines G1R, G2R, · · ·, GmR, wherein:

the pixels connected to the source signal lines S1, S2, · · ·, Sn are supplied with the selection

signals from the first gate signal lines G1L, G2L, · · ·, GmL;

the pixels connected to the source signal lines Sn+1, SN+2, · · ·, S2n are supplied with the selection signals from the second gate signal lines G1R, G2R, · · ·, GmR;

C5 cancel.

the selection signal starts to be supplied to one of the i-th gate signal line GiL and the second gate signal line GiR while the selection signal is supplied to the other one of the first gate signal line GiL and the second gate signal line GiR,

wherein each of the pixel portion, the source driver, the first gate driver and the second gate driver comprises at least one TFT formed over a same substrate.

Cancel Claim 27.